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By: MA

Date:

September 30, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

: 10/047,013

Confirmation No. 5714

Applicant

: Wolfgang Rösner et al.

Filed

: January 16, 2002

TC/A.U.

: 2826

Examiner

: Fazli Erdem

Title:

: Method for Producing a Vertical Semiconductor

Transistor Component and Vertical Semiconductor Transistor Component

Docket No.

: L&L-10232

Customer No. :

24131

Hon. Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Miscellaneous Action dated September 27, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.